160 DISPLAY . 150 MPEG FIG. 1 (PRIOR ART) 133 DESCRAM , 140 ,134 130 MEMORY I/F DRAM ,132 131 EFM DEMOD SERVO UNIT ECC DSP , 170 RF UNIT DISC

5.

FIG. 2 (PRIOR ART)

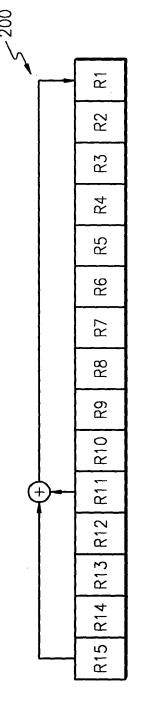


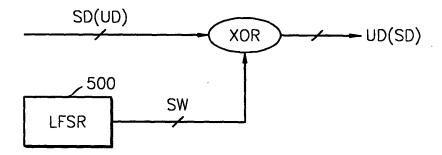
FIG. 3 (PRIOR ART)

INITIAL SETTING VALUE	INITIAL VALUE	INITIAL SETTING VALUE	INITIAL VALUE
НО	0001Н	Н8	Н0100
НI	Н0055	Н6	Н0005
2Н	НZ000	НА	Н0200
ЭН	2А00Н	НВ	2001Н
Н 7	Н≯000	СН	0040H
HS	10049	НО	4002H
Н9	Н8000	EH	Н0800
Н	2800н	НЭ	Н5000

FIG. 4 (PRIOR ART)

LFSR(n)	LFSR(n+1)		
R15	R7		
R14	R6		
R13	R5		
R12	R4		
R11 ·	R3		
R10	R2		
R9	R1		
R8	R15⊕R11		
R7	R14⊕R10		
R6	R13⊕R9		
R5	R12⊕R8		
R4	R11⊕R7		
R3	R10⊕R6		
R2	R9 ⊕R5		
R1	R8 ⊕R4		

FIG. 5



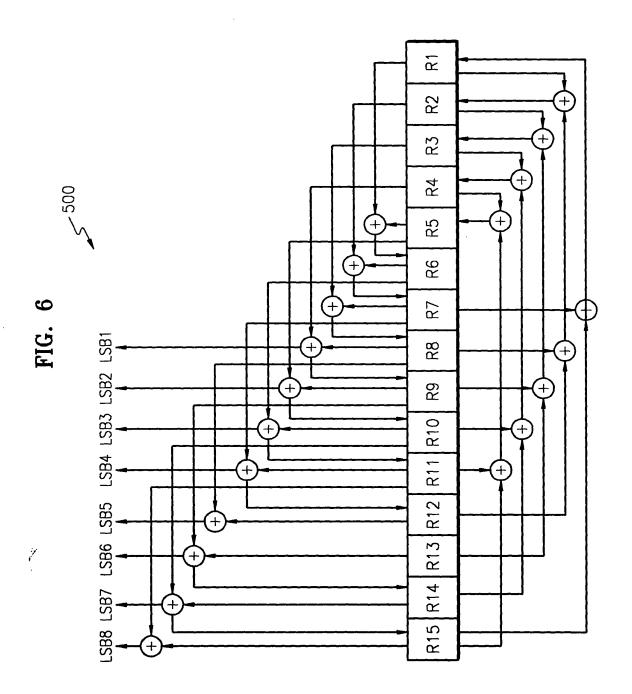


FIG. 7

LFSR(n)	LFSR(n+1)	USB	LSB
R15	R14⊕R10 .		
R14	R13⊕R9		
R13	R12⊕R8		
R12	R11⊕R7		
R11	R10⊕R6		
R10	R9 ⊕R5		
R9	R8 ⊕R4		
R8	R7 ⊕R3	R7 ⊕R3	R15⊕R11
R7	R6 ⊕R2	R6 ⊕R2	R14⊕R10
R6	R5 ⊕R1	R5 ⊕R1	R13⊕R9
R5	R15⊕R11⊕R4	R15⊕R11⊕R4	R12⊕R8
R4	R14⊕R10⊕R3	R14⊕R10⊕R3	R11⊕R7
R3	R13⊕R9 ⊕R2	R13⊕R9 ⊕R2	R10⊕R6
R2	R12⊕R8 ⊕R1	R12⊕R8 ⊕R1	R9 ⊕R5
R1	R15⊕R7	R15⊕R7	R8 ⊕R4